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primarily designed for the Intel

8080 8085 to buffer the data bus

between the microprocessor and

the internal of the Intel 8253 PIT'

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**There are two types of interfacing
in context of the 8085 processor
Memory Interfacing I O Interfacing
Memory Interfacing While
executing an instruction there is a
necessity for the microprocessor
to access memory frequently for
reading various instruction codes
and data stored in the memory'**

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interfacing of 8259 to 8085 is shown in figure is I O mapped in the system ? The low order data bus lines'

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eighty eighty five is an 8 bit microprocessor produced by Intel and introduced in 1976 It is a software binary compatible with the more famous Intel 8080 with only two minor instructions added to support its added interrupt and serial input output features'

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controller 8259 interfacing with

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Features ü To convert the digital

signal to analog signal a Digital to

Analog Converter DAC has to be

employed ü The DAC will accept a

digital binary input and convert to

analog voltage or current' **8253**

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used to interface the 8253 54 to the

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Interfacing is considered to be a
main part of Microprocessor 5
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microprocessor is to connect it
with various peripherals to
perform various operations to
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